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USDP - User Manual -- Ver 2 - rev3

density of logic) These advanced devices also offer features such as built-in hardwired IP cores (such as the IBM Power PC, PCI cores, microcontrollers, peripherals, etc), substantial amounts of memory, clock management systems, and support for many of the latest, very ...

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MATrix-II Manual - 01V1

MATrix-II supports multiple vendor devices from Xilinx and Altera, who are world leader in PLD manufacturing The MATrix-II supports Spartan-2 and XC9500 series of devices from Xilinx; and ACEX 1K and MAX7000s series of devices from Altera The basic version of MATrix-II comes with 50,000 gate XC2S50-PQ208 FPGA,